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PATENT

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Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 16, 2005

William R. Allen
William R. Allen, Reg. No. 48,389

16 September 2005
Date

Applicants: Toshiharu Furukawa et al.
Serial No.: 10/767,065
Filed: January 29, 2004
Art Unit: 2811
Examiner: Ori Nadav
Confirmation No.: 5663
Title: VERTICAL NANOTUBE SEMICONDUCTOR DEVICE
STRUCTURES AND METHODS OF FORMING SAME
Atty Docket No.: IBM-269

Cincinnati, Ohio 45202

September 16, 2005

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of candor and good faith imposed by 37 C.F.R. §1.56 and means of complying therewith according to 37 C.F.R. §§1.97 and 1.98, the references listed on the attached Form PTO-1449 are called to the attention of the United States Patent and Trademark Office in connection with the above-identified patent application.

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Subject to the Office's waiver of providing copies of U.S. patents and published applications for applications filed after June 30, 2003, only foreign references and articles are provided, since this application was filed on January 29, 2004, and therefore, subsequent to June 30, 2003.

The Examiner is urged to consider all of the cited documents and to make an independent evaluation of the teachings and materiality of each.

Applicants also enclose a check in the amount of \$180.00 for this submission. If further fees are due, the Commissioner is authorized to charge deposit account 23-3000 for any required fees.

Respectfully submitted,

WOOD, HERRON & EVANS, L.L.P.

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SUBSTITUTE FORM PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. IBM/269		SERIAL NO. 10/767,065	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANT Toshiharu Furukawa et al.		FILING DATE January 29, 2004	
(37 CFR 1.98(b))				GROUP 2811			

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.A						
	A.B						
	A.C						
	A.D						
	A.E						
	A.F						
	A.G						
	A.H						
	A.I						
	A.J						
	A.K						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS							
		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	A.L	WO 2004/O40668	March 14, 2004	PCT	H01L 51/20		Abstract Only
	A.M	WO 2004/O40616	March 13, 2004	PCT	H01L		Abstract Only
	A.N	WO 2004/105140	December 2, 2004	PCT	H01L 29/786		Abstract Only
	A.O	EP 1420414	April 6, 2005	EP	G11C 13/02	H01L 51/30	N/A
	A.P						
	A.Q						

OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)		
	A.R	Furukawa et al., <u>United States Patent Application Publication No. US 2005/O167740</u> , Publication Date: August 4, 2005
	A.S	Furukawa et al., <u>United States Patent Application Publication No. US 2005/O179029</u> , Publication Date: August 18, 2005
	A.T	Furukawa et al., <u>United States Patent Application Publication No. US 2005/O130341</u> , Publication Date: June 16, 2005

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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	B.M						
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	B.R	Furukawa et al., <u>United States Patent Application Publication No. US 2005/O129948</u> , Publication Date: June 16, 2005					
	B.S	Dubin et al., <u>United States Patent Application Publication No. US 2005/O167755</u> , Publication Date: August 4, 2005					
	B.T	Roesner et al., <u>United States Patent Application Publication No. US 2003/O132461</u> , Publication Date: July 17, 2003					
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	C.M						
	C.N						
	C.O						
	C.P						
	C.Q						
OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)							
	C.R	Choi et al., <u>United States Patent Application Publication No. US 2002/O001905</u> , Choi et al.					
	C.S	Georg S. Duesberg et al., <u>Large-Scale Integration of Carbon Nanotubes into Silicon Based Microelectronics</u> , Proceedings of SPIE Vol. 5118 (2003), pgs. 125-137					
	C.T						
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